



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

C. J. Smith
TH4

Applicant: Mark R. Thomann et al.

Examiner: Shelly A. Chase

Patent No.: 5,964,896

Group Art Unit: 2784

Issue Date: October 12, 1999

Docket No.: 303.012US2

Title: METHOD AND APPARATUS FOR A HIGH SPEED CYCLICAL REDUNDANCY CHECK
SYSTEM

Commissioner of Patents and Trademarks
Washington, D.C. 20231

CERTIFICATE

APR 19 2000

We are transmitting herewith the attached:

OF CORRECTION

- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.
- Other: ____

Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this Transmittal Letter and the paper, as described above, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on this 28 day of March, 2000.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By: Ann M. McCrackin
Name: Ann M. McCrackin
Reg. No. 42,858
AMM1:CMG:gmb

Patent 5,964,896

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 5,964,896

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Issue Date: October 12, 1999

Patentee: Mark R. Thomann et al.

Title: METHOD AND APPARATUS FOR A HIGH SPEED CYCLICAL
REDUNDANCY CHECK SYSTEM

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner of Patents and Trademarks
Washington, D.C. 20231

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

As the error is that of the Patent Office, it is believed that no fee is due.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

MARK R. THOMANN ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
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Minneapolis, MN 55402
(612) 349-9592

Date March 28, 2000

By Ann M. McCrackin

Ann M. McCrackin
Reg. No. 42,858
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner of Patents and Trademarks, Washington, D.C. 20231 on March 28, 2000.

Ann M. McCrackin
Name

Ann M. McCrackin
Signature

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION****PATENT NO.:** 5,964,896

Page 1 of 1

DATED: October 12, 1999**INVENTOR(S):** Thomann et al

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 8, line 45, insert --15. The programmable error detection and correction sytem of claim 6, wherein the control module programs a fourth parallel data bus to transfer data between a second data port and the edit buffer.--



MAILING ADDRESS OF SENDER:
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
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Minneapolis, MN 55402

PATENT NO. 5,964,896
Docket No. 303.012US2

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.: 5,964,896

Page 1 of 1

DATED: October 12, 1999

INVENTORS : Mark R. Thomann, Huy Thanh Vo, Charles L. Ingalls

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 8, line 45 insert the following:

--15. A bidirectional programmable error detection and correction system comprising:

- an edit buffer programmable to support a plurality of error processing modes;
- an error check module programmable for generating and comparing error check words;
- a first parallel data bus programmable for transferring data from the edit buffer to a first data port;
- a second parallel data bus programmable for transferring data from the first data port to the error check module and to the edit buffer, and further programmable for transferring data from the edit buffer to the error check module;
- a third parallel data bus programmable for transferring an error check word between the error check module and the edit buffer;
- a control module for programming the first, second and third data buses, and the error check module to operate according to the plurality of error processing modes, wherein data transferred from the edit buffer to the first data port is checked for errors and an error check work is generated for data transferred from the first data port to the edit buffer.--

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INVENTORS : Mark R. Thomann, Huy Thanh Vo, Charles L. Ingalls

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--15. A bidirectional programmable error detection and correction system comprising:

an edit buffer programmable to support a plurality of error processing modes;
an error check module programmable for generating and comparing error check

words;

a first parallel data bus programmable for transferring data from the edit buffer to a first data port;

a second parallel data bus programmable for transferring data from the first data port to the error check module and to the edit buffer, and further programmable for transferring data from the edit buffer to the error check module;

a third parallel data bus programmable for transferring an error check word between the error check module and the edit buffer;

a control module for programming the first, second and third data buses, and the error check module to operate according to the plurality of error processing modes, wherein data transferred from the edit buffer to the first data port is checked for errors and an error check work is generated for data transferred from the first data port to the edit buffer.--

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